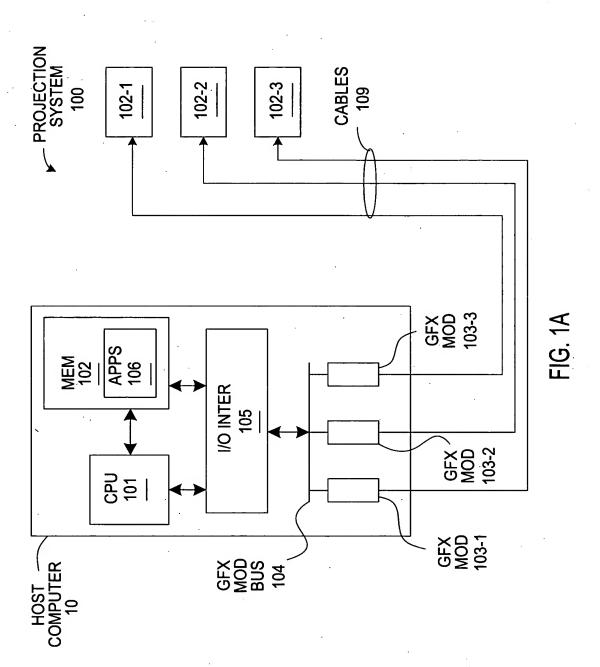
Page 1 of 27

1/27

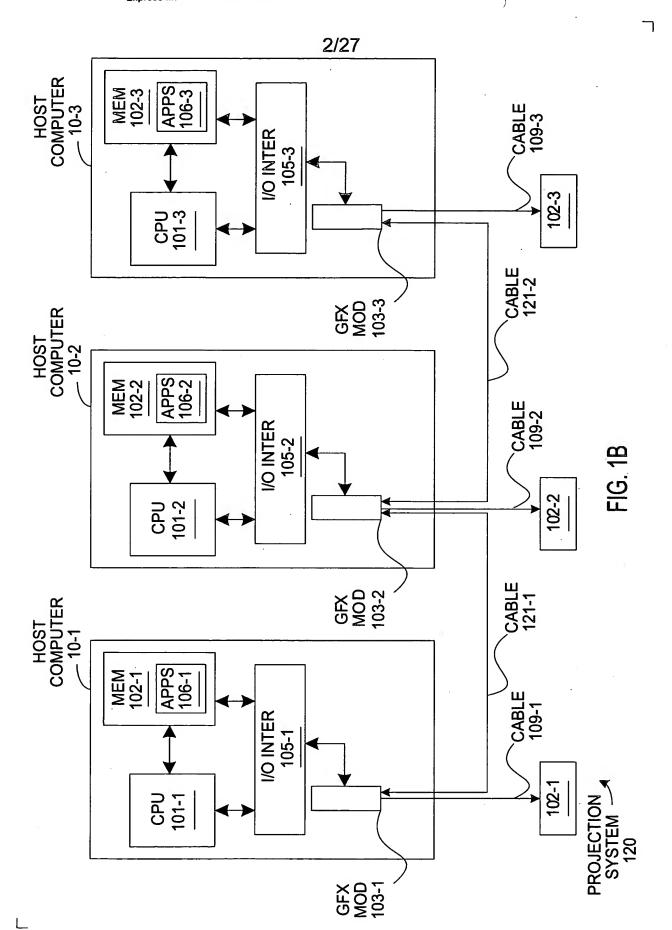


Atty Dkt. No.: U.S. Serial No.: Filed: Applicant: Title: NVDA/P000737 UNKNOWN HEREWITH

NVIDIA Corporation
Method For Synchronizing Graphics Processing Units

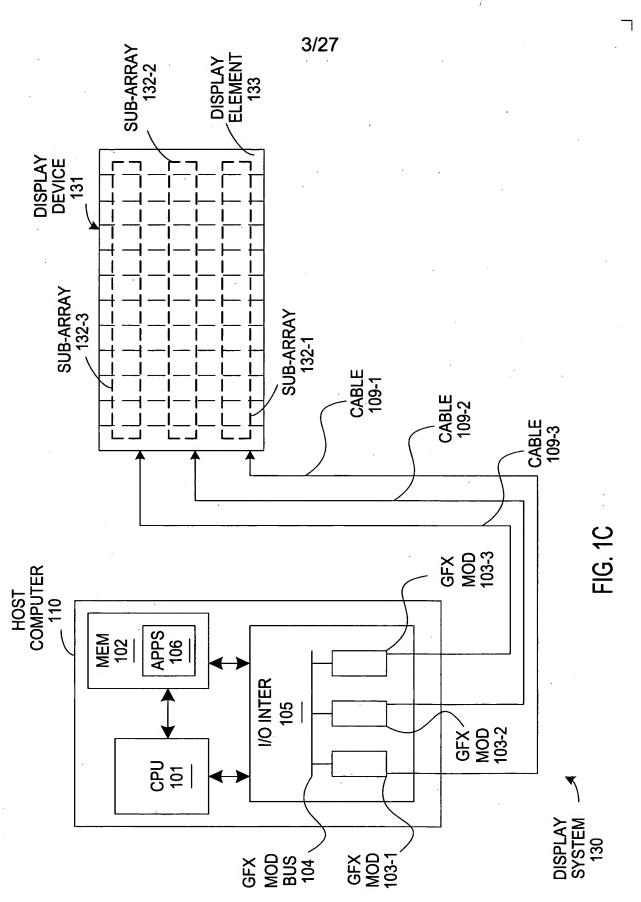
Inventor: lan M. Williams, ET AL. Express Mail No.: EV335471096US

Page 2 of 27

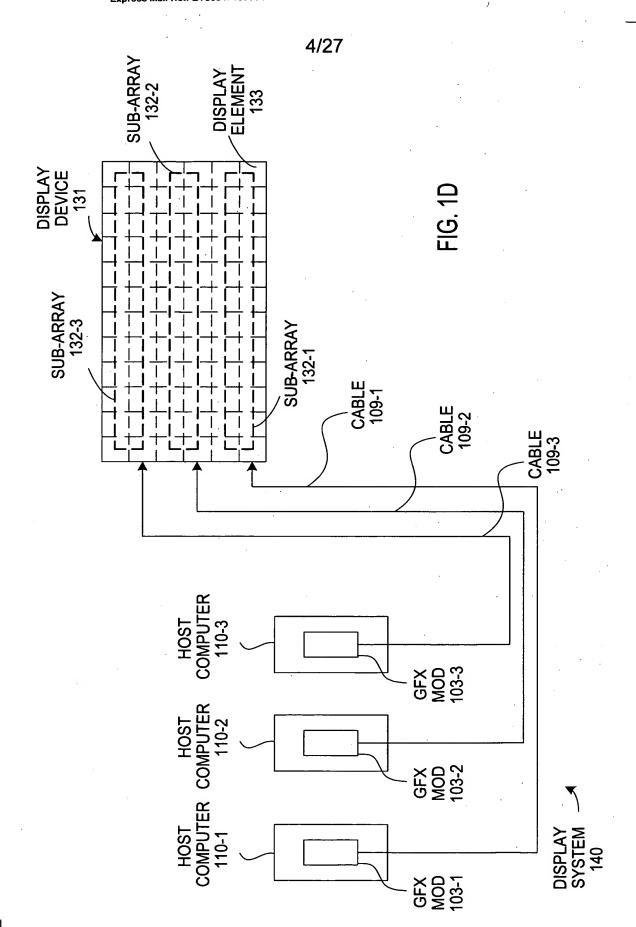


Atty Dkt. No.: U.S. Serial No.: Filed: Applicant: Title: NVDA/P000737 UNKNOWN HEREWITH

Applicant: NVIDIA Corporation
Title: Method For Synchronizing Graphics Processing Units
Inventor: Ian M. Williams, ET AL.
Express Mall No.: EV335471096US Page Page 3 of 27

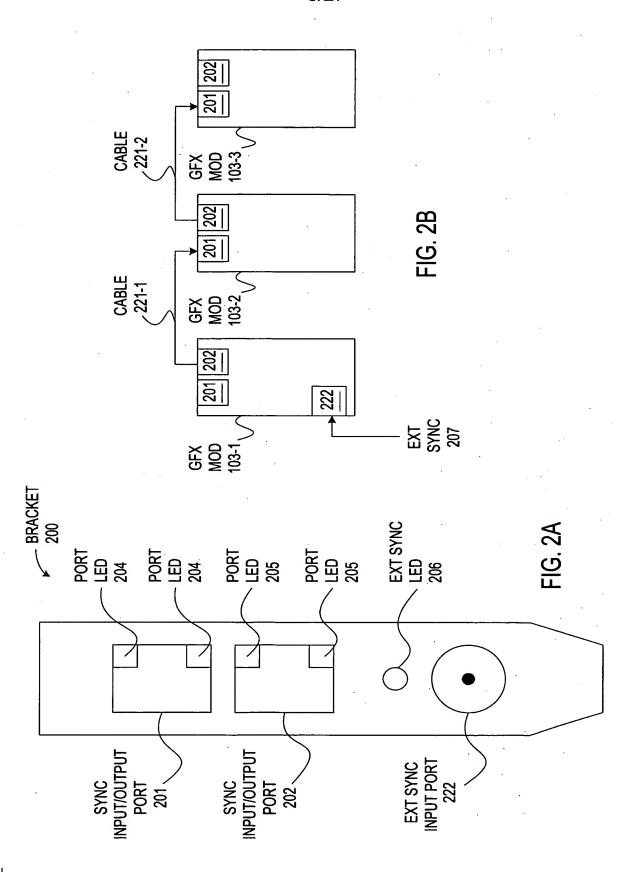


Page 4 of 27



Atty Dkt. No.: U.S. Serial No.: Filed: Applicant: Title: NVDA/P000737 UNKNOWN HEREWITH Applicant: NVIDIA Corporation
Title: Method For Synchronizing Graphics Processing Units
Inventor: Ian M. Williams, ET AL.
Express Mail No.: EV335471096US Page

Page 5 of 27



Page 6 of 27

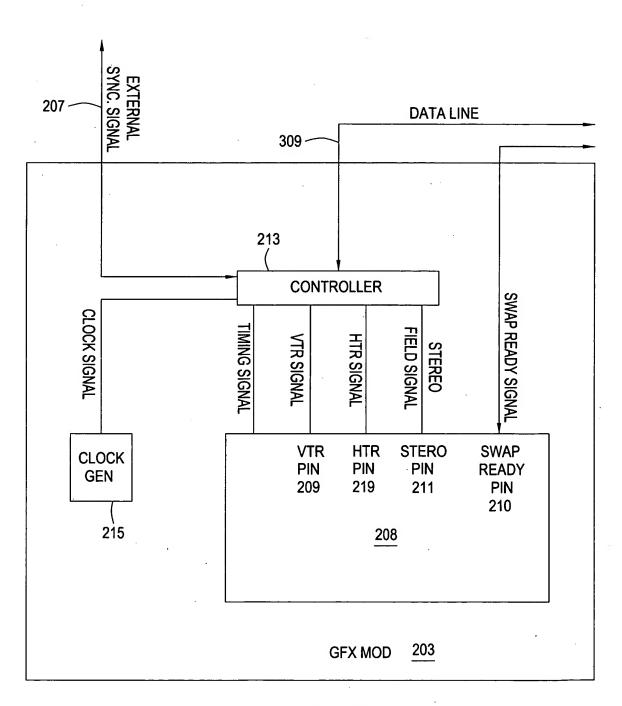
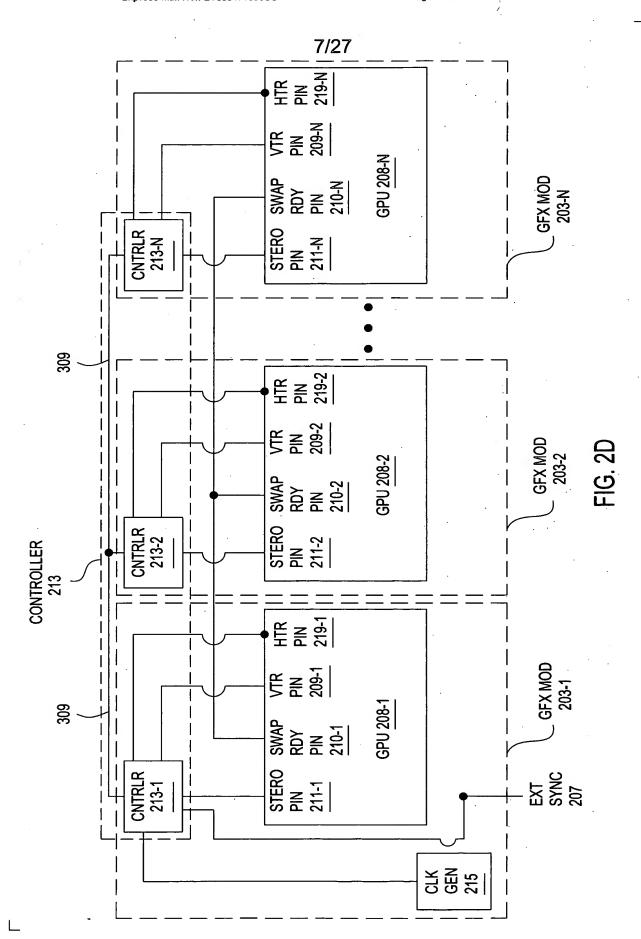


FIG. 2C

Atty Dkt. No.: U.S. Serial No.: Filed: NVDA/P000737 UNKNOWN HEREWITH

Applicant: NVIDIA Corporation
Title: Method For Synchronizing Graphics Processing Units
Inventor: Ian M. Williams, ET AL.
Express Mail No.: EV335471096US Page

Page 7 of 27



Page 8 of 27

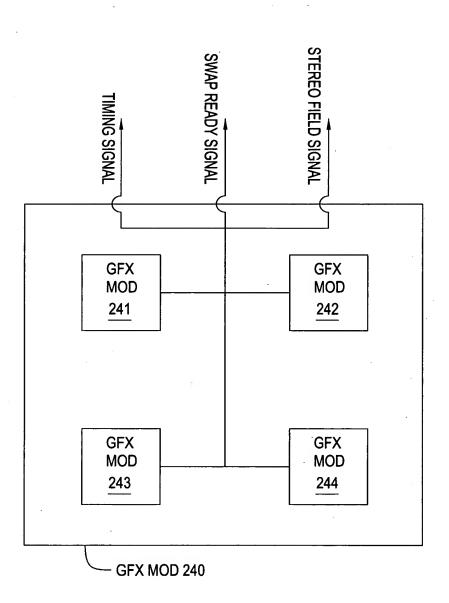


FIG. 2E

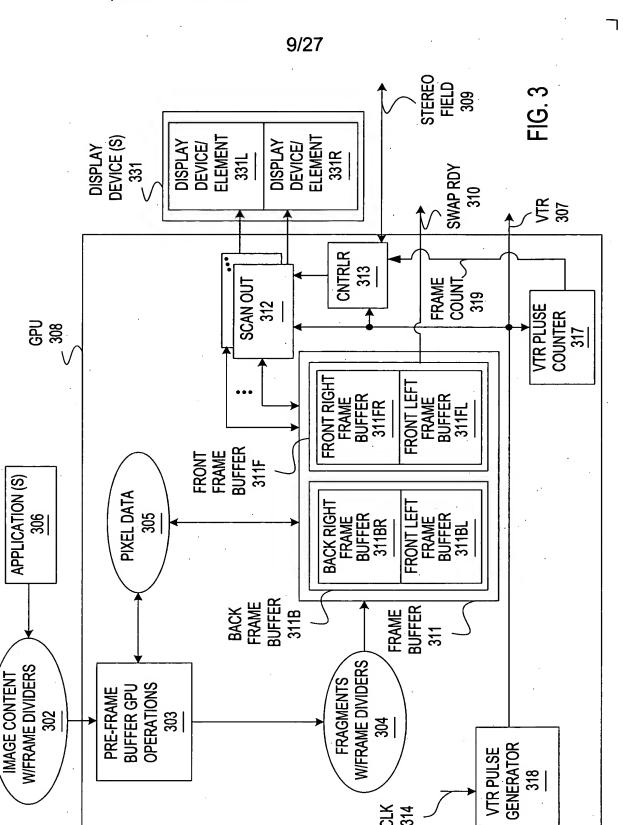
Atty Dkt. No.: U.S. Serial No.: Filed: NVDA/P000737 UNKNOWN HEREWITH

Applicant: NVIDIA Corporation
Title: Method For Synchronizing Graphics Processing Units
Inventor: Ian M. Williams, ET AL.
Express Mail No.: EV335471096US Page

Page 9 of 27

318

314 314



Page 10 of 27

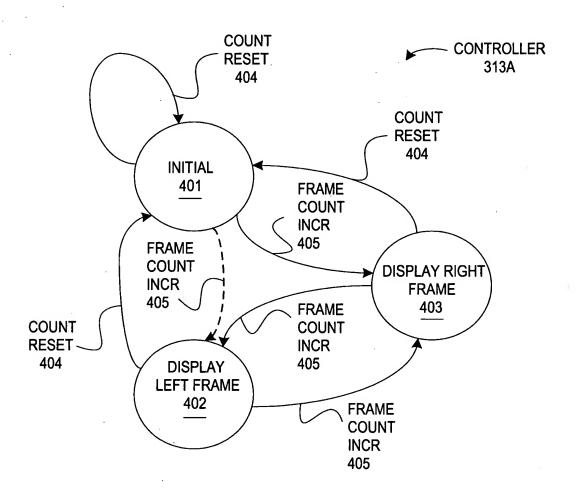
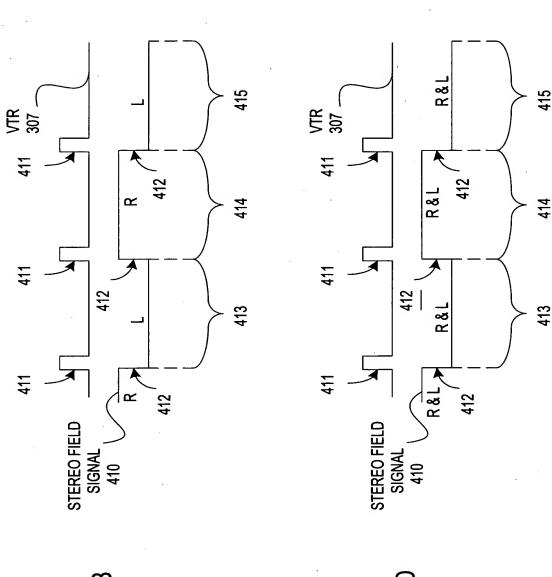


FIG. 4A

Page 11 of 27

11/27



Г

Page 12 of 27

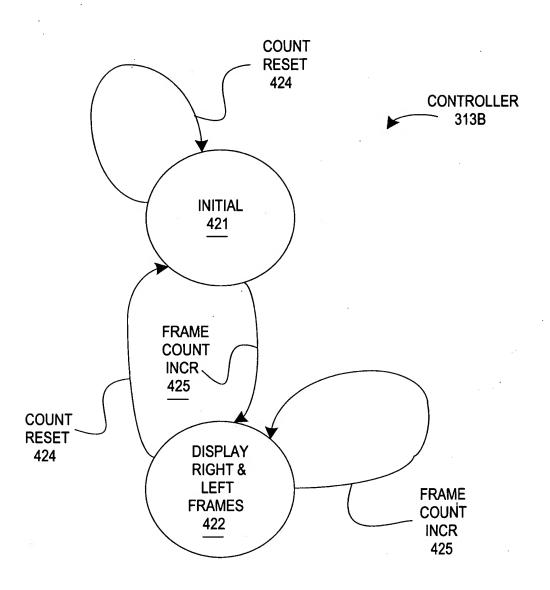


FIG. 4C

Page 13 of 27

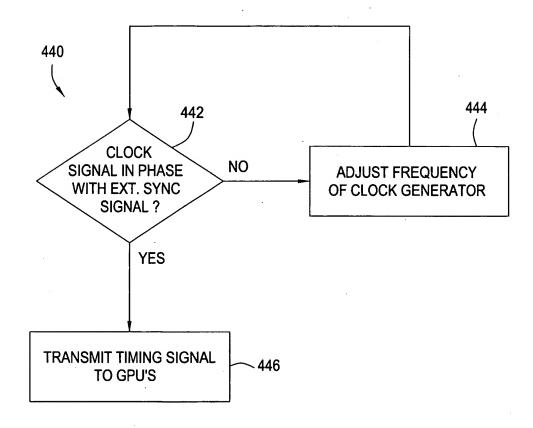


FIG. 4E

Atty Dkt. No.: NVDA/P000737
U.S. Serial No.: UNKNOWN
Filed: HEREWITH
Applicant: NVIDIA Corporation

Title: Method For Synchronizing Graphics Processing Units Inventor: Ian M. Williams, ET AL.

Inventor: Ian M. Williams, ET AL. Express Mail No.: EV335471096US

Page 14 of 27

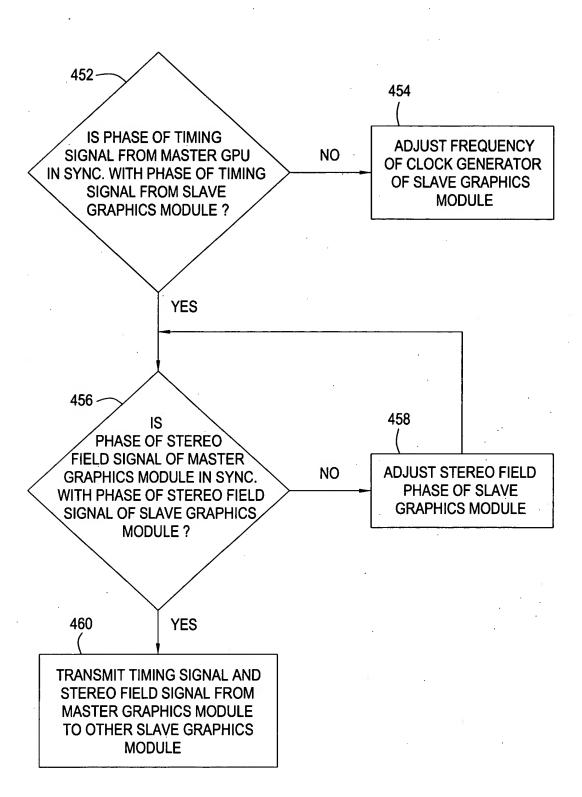


FIG. 4F

Atty Dkt. No.: U.S. Serial No.: Filed:

Г

NVDA/P000737 UNKNOWN HEREWITH **NVIDIA Corporation** 

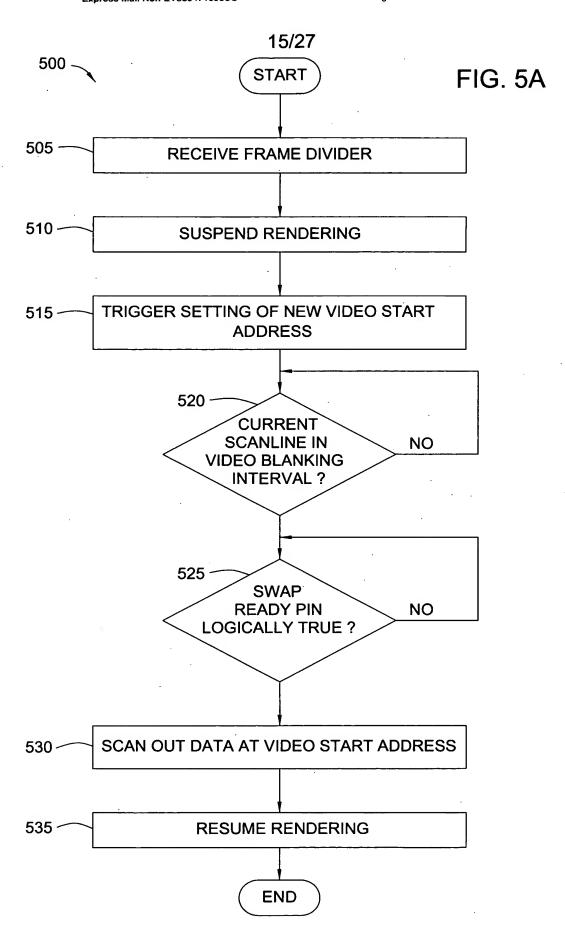
Applicant: Title: Method For Synchronizing Graphics Processing Units

lan M. Williams, ET AL.

Inventor:

Express Mail No.: EV335471096US

Page 15 of 27



Atty Dkt. No.: U.S. Serial No.: Filed:

Г

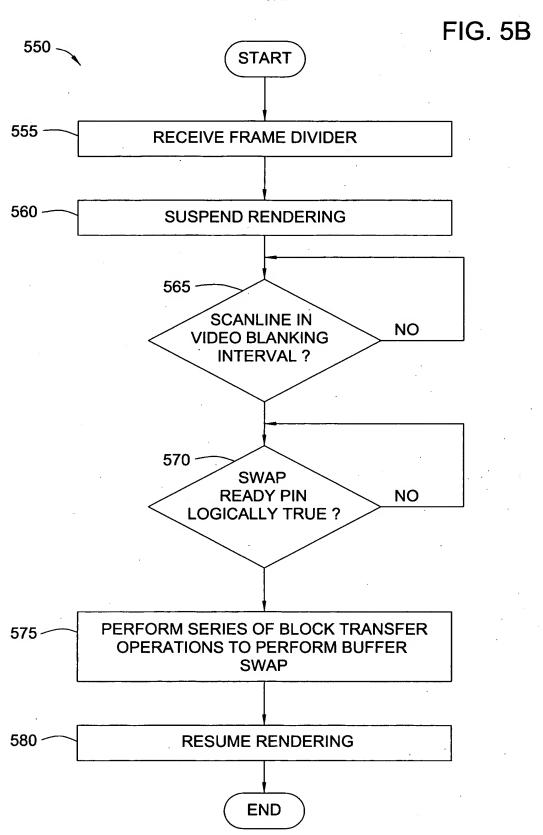
Applicant: Title: Inventor:

NVDA/P000737
UNKNOWN
HEREWITH
NVIDIA Corporation
Method For Synchronizing Graphics Processing Units Ian M. Williams, ET AL.

Express Mail No.: EV335471096US

Page 16 of 27





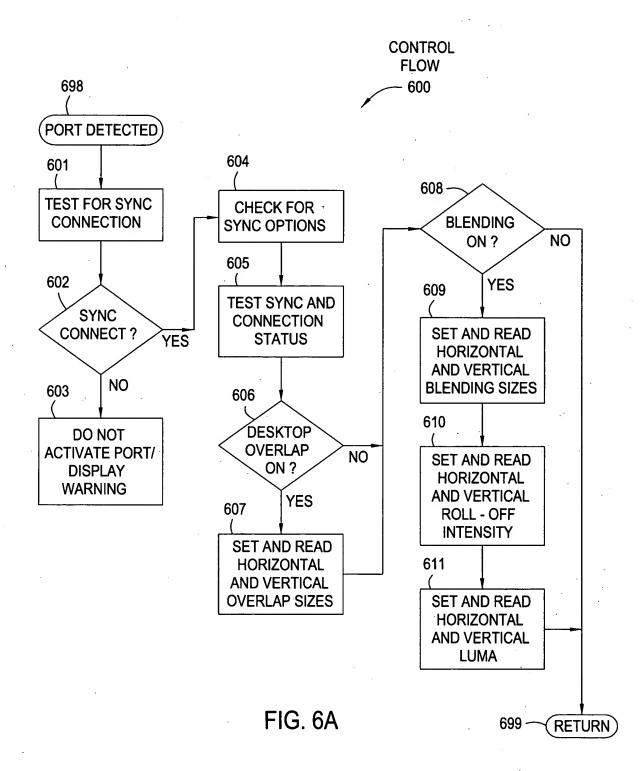
Atty Dkt. No.: U.S. Serial No.: NVDA/P000737 UNKNOWN Filed: HEREWITH Applicant: Title: **NVIDIA Corporation** 

Inventor: lan M. Williams, ET AL.

Method For Synchronizing Graphics Processing Units

Express Mail No.: EV335471096US

Page 17 of 27



Atty Dkt. No.: U.S. Serial No.: NVDA/P000737 UNKNOWN Filed: HEREWITH

Applicant: NVIDIA Corporation
Title: Method For Synchronizing Graphics Processing Units
Inventor: Ian M. Williams, ET AL.
Express Mail No.: EV335471096US Pag

Page 18 of 27

18/27

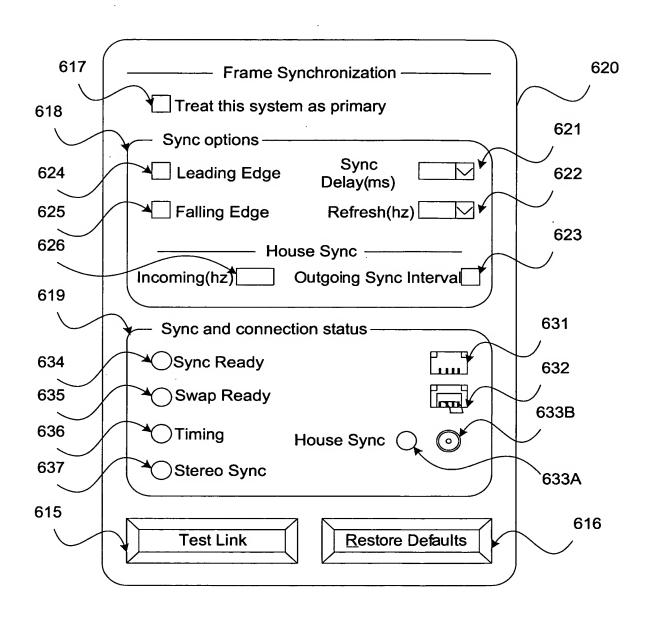
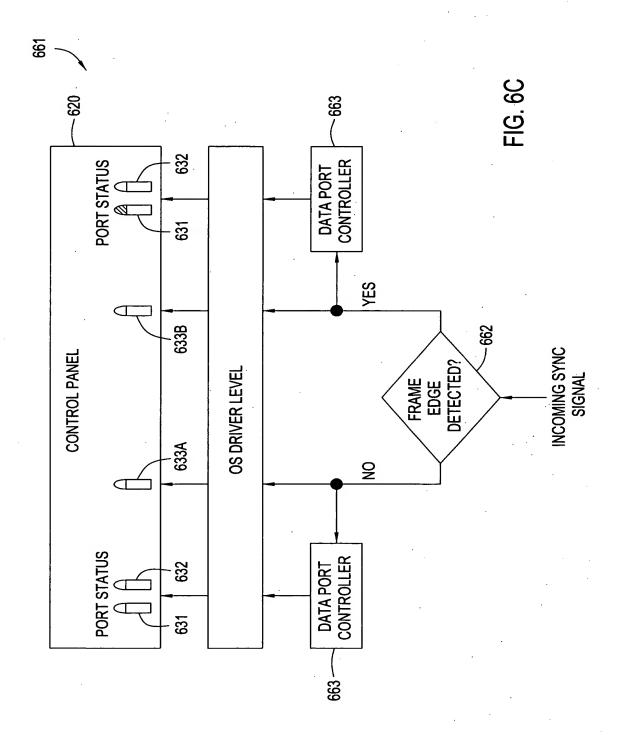
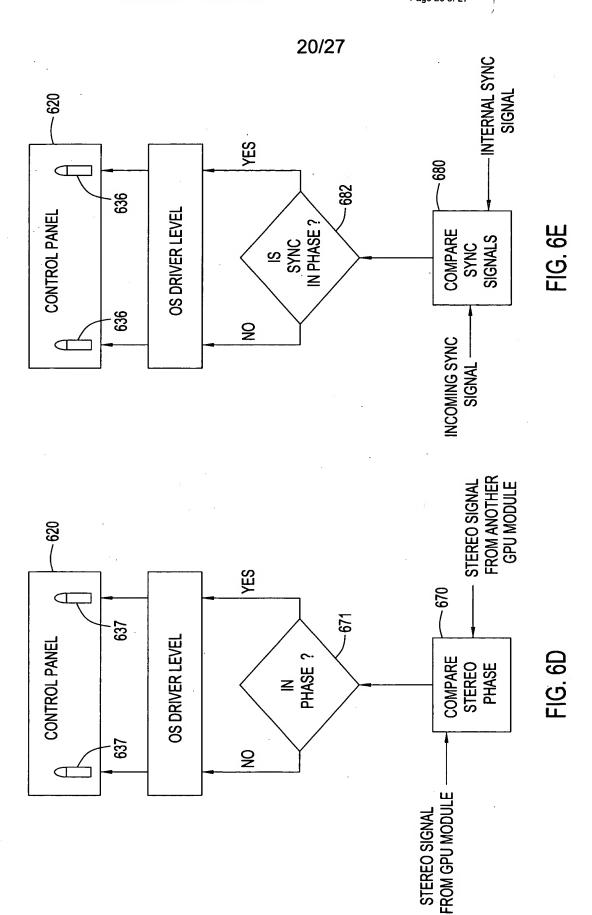


FIG. 6B

Page 19 of 27



Page 20 of 27



NVDA/P000737 UNKNOWN Atty Dkt. No.: U.S. Serial No.: Filed: HEREWITH Applicant: Title: **NVIDIA Corporation** 

Method For Synchronizing Graphics Processing Units

Inventor: Ian M. Williams, ET AL. Express Mail No.: EV335471096US

Page 21 of 27

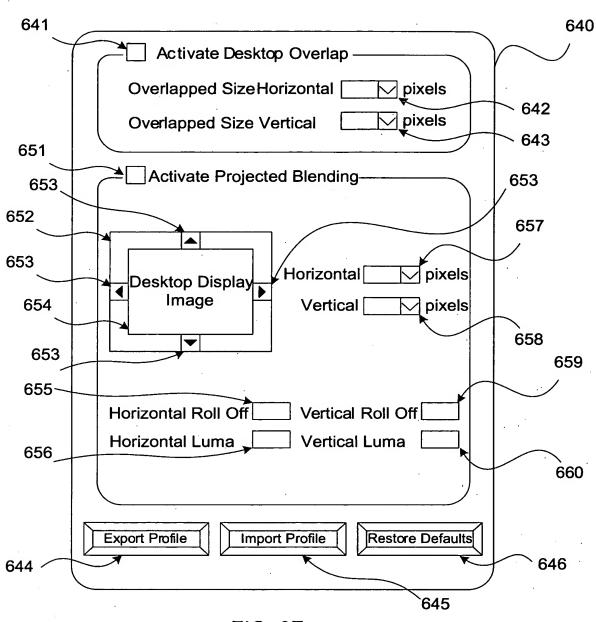
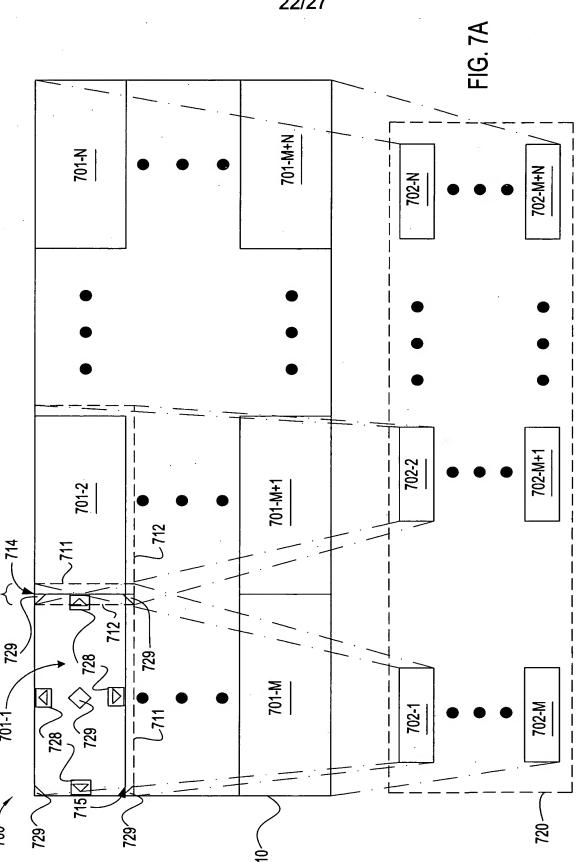


FIG. 6F

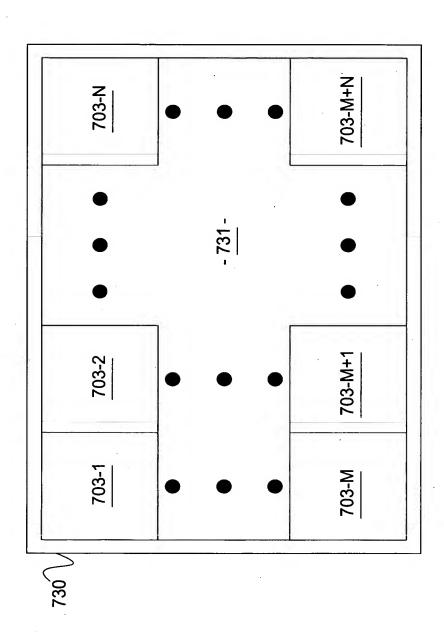
Atty Dkt. No.: NVDA/P000737
U.S. Serial No.: UNKNOWN
Filed: HEREWITH
Applicant: NVIDIA Corporation
Title: Method For Synchronizing Graphics Processing Units
Inventor: Ian M. Williams, ET AL.
Express Mall No.: EV335471096US Pag Atty Dkt. No.: U.S. Serial No.: Filed: Applicant: Title: Inventor:

Page 22 of 27

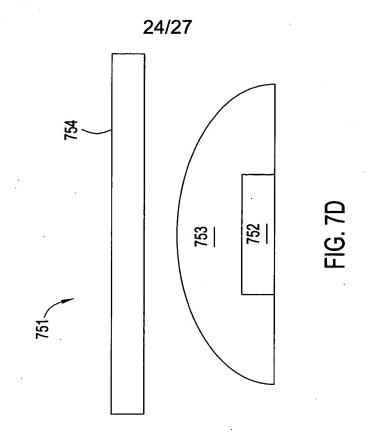


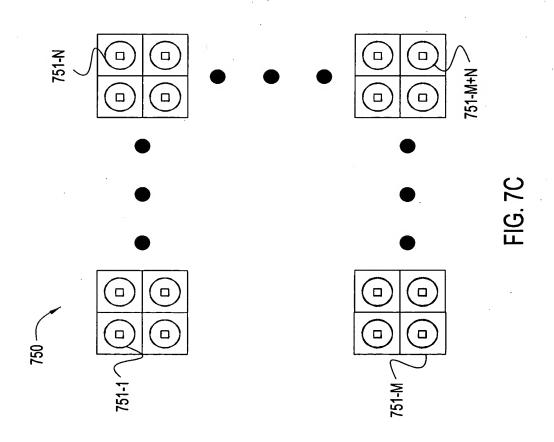
Page 23 of 27

23/27



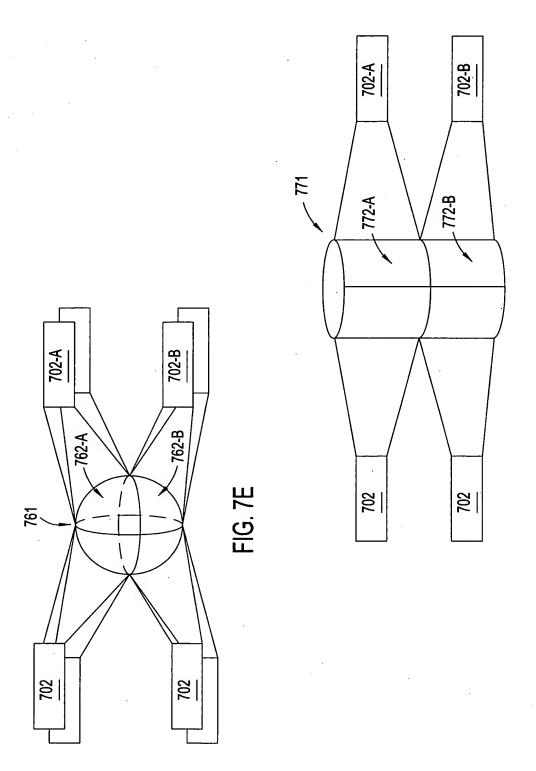
Page 24 of 27





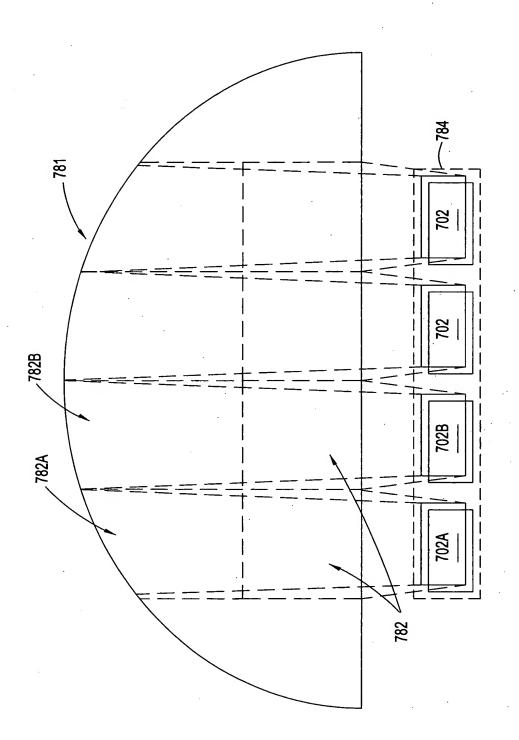
Page 25 of 27

25/27



Page 26 of 27

26/27



Page 27 of 27

27/27

